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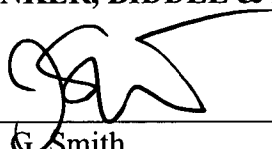
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Respectfully submitted,

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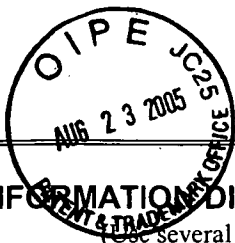


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Applicants

Yasuhiro SUZUKI, et al.

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U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub Class	Translation	
						YES	NO
/R.B./	09-270960	Oct. 14, 1997	Japan			Abstract	
↓	2000-324397	Nov. 24, 2000	Japan			Abstract	
	2002-051264	Feb. 15, 2002	Japan			Abstract	
	09-238286	Sept. 9, 1997	Japan			Abstract	
	2000-032342	Jan. 28, 2000	Japan			Abstract	
↓	09-051476	Feb. 18, 1997	Japan			Abstract	
	WO 01/11870	Feb. 15, 2001	PCT			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/R.B./	Garverick et al., "A 32-Channel Charge Readout IC for Programmable, Nonlinear Quantization of Multichannel Detector Data," IEEE Journal of Solid-State Circuits, Vol. 30, No. 5, May 1995, pp. 533-541

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